

Special Theme Issue devoted to Current trends in the Electronics

Call for Papers

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The **Advances in Electrical and Electronic Engineering** journal invites authors to submit articles into the special theme issue focused on recent developments in the **Electronics-related topics**. Both reviews and research papers are warmly welcome. This issue is planned to be published in **December 2017**. The purpose of publishing this issue is to present a collection of original papers that gives an overview of the current progress in research, development, and applications in the field of **Electronics**.

Scope of the special theme issue:

The major topics of interest include (but topics are not limited to):

- Circuit-theoretic aspects of traditional and nano-electronics
- Memristors and Memristive Systems
- VLSI circuits and systems
- New current-mode, voltage-mode, and mixed-mode active building blocks
- Analysis and design of circuits for analog signal processing
- Low-power, low-component-count current-mode circuits and systems
- Modelling and simulation of complex systems

Important Dates:

Submission Deadline	June 30, 2017
Review Result Notification	August 31, 2017
Final Manuscript Due	September 30, 2017
Acceptance Notification	November 30, 2017
Tentative Publication Date	December 31, 2017



SJR₂₀₁₅: 0.207 SNIP₂₀₁₅: 0.819



<http://advances.utc.sk>

Submission Instructions:

The authors are encouraged to read carefully the information "How to submit an article" on the web pages of the journal **Advances in Electrical and Electronic Engineering** on the following link:

<http://advances.utc.sk/index.php/AEEE/about/submissions#authorGuidelines>.

Also, animations in flash and PDF formats are available, guiding the potential authors step-by-step throughout the submission procedure in the Editorial Open Journal System. In case there are any doubts regarding the submission or related procedures, the authors are requested to contact directly the journal Editor or journal support via email: jan.latal@vsb.cz or advances@vsb.cz.

Preferred formats for your manuscript are:

- doc, docx, rtf, odt.

PDF file format is accepted only if you write your article in LaTeX (PDF file is uploaded as article, but LaTeX version of your article together with all figures, tables are uploaded as supplementary files). Other formats are **not accepted**. The designation and description of images have to be according to the template for authors. Due to the final processing of the manuscript in **LaTeX**, we prefer to deliver all pictures, charts and diagrams as supplementary files in **vector formats**. All the potential authors interested in publishing their scientific work in this special theme issue are requested to write the text **"Special Theme Issue on The Current trends in the Electronics"** in the section **"Comments for Reviewers"** during the article submission. There are **no page charges** for the publication in this issue. If the paper is conditionally accepted, the time allowed for its revision will be shorter than for the regular issue (which is normally 30 days), due to a tight production schedule. Accepted for this special theme issue will only be papers submitted via the electronic online submission system of the journal.

Guest Editors:

Dalibor Biolek (University of Defense & Brno University of Technology, Czech Republic).

Dalibor Biolek received the M.Sc. degree in Electrical Engineering from Brno University of Technology, Czech Republic, in 1983, and the PhD. degree in Electronics from the Military Academy Brno, Czech Republic, in 1989, focusing on algorithms of the symbolic and numerical computer analyses of electronic circuits with a view to the linear continuous-time and switched filters. He is currently with the Dept. of EE, University of Defense Brno (UDB), and with the Dept. of Microelectronics, Brno University of Technology (BUT), Czech Republic. His scientific activity is directed to the areas of general circuit theory, frequency filters, mem-systems, and computer simulation of electronic systems. He has published over 400 papers and is author of several books on circuit analysis and simulation. At present, he is a professor at BUT and UDB in the field of Theoretical Electrical Engineering. Prof. Biolek is a Senior Member of the CAS/COM Czech National Group of IEEE. He serves as an Associate Editor of Electronics Letters. He is also a member of International Scientific Editorial Board of Advances in Electrical and Electronic Engineering and Radioengineering journal.

Raj Senani (Netaji Subhas Institute of Technology, India).

Raj Senani is an eminent academician and researcher in the field of electronics engineering. Apart from teaching courses in the areas related to Analog Circuit Design, he has made significant contributions to the fields of Analog integrated circuits, Analog signal processing and Analog VLSI Circuits, in the course of his research work. He has published over 150 papers and three research monographs in these areas and is currently serving as Editor-in-Chief of IETE Journal of Education and an Associate Editor of Circuits, Systems and Signal Processing. He served as the Director of Netaji Subhas Institute of Technology recently during October 2008-December, 2014 and earlier during 1996-1997 and 2003-2004. He has been continuing as a Professor in ECE Division of NSIT and in addition, he is also functioning as an Adjunct Professor for National Institute of Technology (NIT), Patna. Professor Senani is a senior member of IEEE, was elected Fellow of National Academy of Sciences India (NASI) in 2008 and is the recipient of 25th Khwarizmi International Award for 2012.

Data Ram Bhaskar (Delhi Technological University, India).

D.R. Bhaskar received B.Sc. from Agra University, B.Tech. from Indian Institute of Technology (IIT), Kanpur, M.Tech. from IIT, Delhi and Ph.D. from University of Delhi. Dr. Bhaskar held the positions of Assistant Engineer in DESU (1981–1984), Lecturer (1984–1990) and Senior Lecturer (1990–1995) at the EE Department of Delhi College of Engineering, Kashmere Gate, Delhi-110006 and Reader in ECE Department of Jamia Millia Islamia (1995–2002). He became a full Professor in January 2002 and has served as the Head of the Department of ECE during 2002–2005. Since August 2016, he is working as Professor, Department of ECE, Delhi Technological University, India. Professor Bhaskar's teaching and research interests are in the areas of Analog Integrated Circuits and Signal Processing, Communication Systems and Electronic Instrumentation. He has authored/co-authored 86 research papers in various International journals, 4 book chapters and 03 research monographs published by Springer. He has acted/has been acting as a Reviewer for several international journals. Professor Bhaskar is a Senior Member of IEEE and Editor of IETE Journal of Education (India). His biography is included in a number of international biographical directories.